

## Pixel-LEDs

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### APA102 chip built in SMD5050

#### Features and Benefits

- Intelligent reverse connect protection, the power supply reverse connection does not damage the IC.
- The control circuit and the LED share the only power source.
- Control circuit and RGB chip are integrated in a package of 5050 components, form a complete control of pixel point.
- Built-in signal reshaping circuit, after wave reshaping to the next driver, ensure wave-form distortion not accumulate.
- Built-in electric reset circuit and power lost reset circuit.
- Each pixel of the three primary color can achieve 256 brightness display, completed 16777216 color full color display, and scan frequency not less than 400Hz/s.
- Cascading port transmission signal by (DAT CLK) two line.
- Any two point the distance less than 5m transmission signal without any increase circuit.
- When the refresh rate is 30fps, cascade number are not more than 512 pixels
- Send data at speeds of 800Kbps or 1200Kbps
- The color of the light were highly consistent, cost-effective..

#### General description

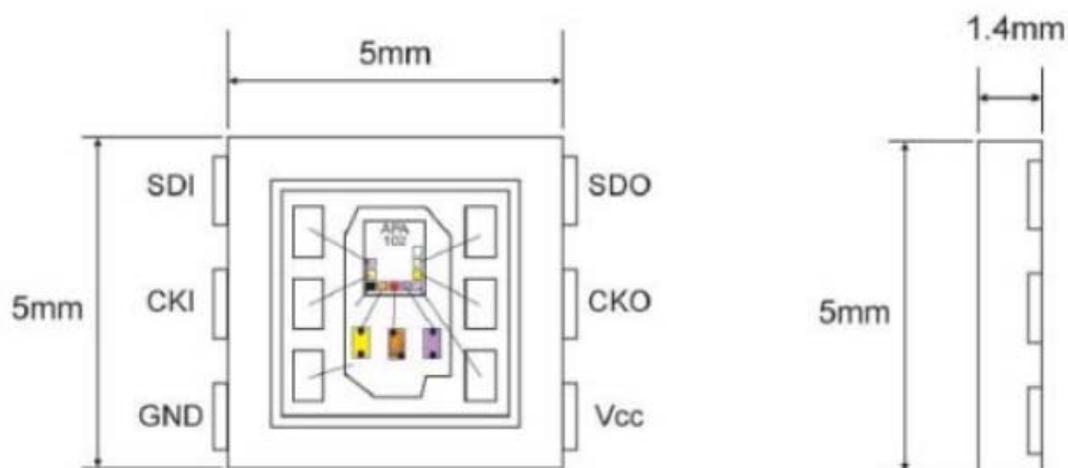
APA102 is a intelligent control LED light source that the control circuit and RGB chip are integrated in a package of 5050 components. It internal include 3 groups shift register and Self-detection signdecoder circuit. Also include a 4.5V voltage regulator part and continuous oscillator effectively ensuring the pixel point light color height consistent.

The data transfer protocol use two line decoder mode. After the pixel power-on reset, the DIN port receive data from controller after decoder , the first pixel collect initial 24bit data then sent to the internal data register, the other data which reshaping by the internal signal decoder circuit sent to the next cascade pixel through the DO CO port. After transmission for each pixel.

LED with low driving voltage, environmental protection and energy saving, high brightness, scattering angle is large, good consistency, low power, long life and other advantages. The control chip integrated in LED above becoming more simple circuit, small volume, convenient installation.

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Size:



PIN function

NO.	Symbol	Function	description
1	SDI	Data	Input
2	CKI	Clock	Input
3	SDO	Data	Output
4	CKO	Clock	Output
3	GND	Ground	
4	VCC	+	5V

Peak value

Parameter	Symbol	Ratings	Unit
Power supply	voltage	VDD+4.5~+5.5	V
Input voltage	VI	-0.5~VDD+0.5	V
Operation junction Temp.	Topt	-40~+70	°C
Storage Temp. range	Tstg	-40~+100	°C

Electrical Characteristics (TA=-20 ~ +70°C , VDD=4.5 ~ 5.5V, VSS=0V, unless otherwise specified)

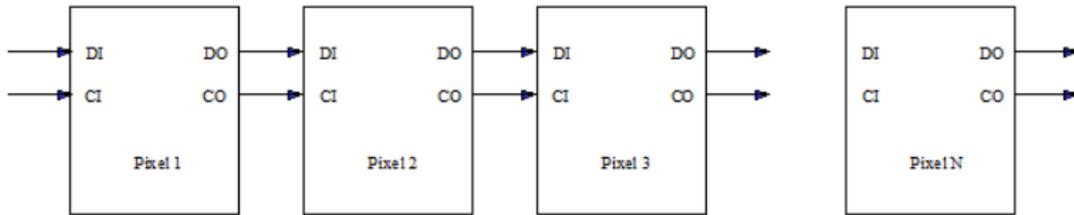
Parameter	Symbol	conditions	Min	Tpy	Max	Unit
Supply Voltage	VDD			5.0	5.5	V
Input Voltage High	VIH		0.7VDD		VDD+0.3	V

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Input Low Volt.	VIL		VSS-0.3		0.3VDD	V
Source Current	I	VDD=5V	19.5	20	20.5	mA
Pull High	RIN	VDD=5V		570		KΩ
Regulator volt.	VREG	VDD > 5V	4.4	4.5	4.7	V
Oscillator	FOSC		800		1200	KHZ

### RGB IC characteristic parameter

Emitting color	Wavelength(nm)	Luminous intensity(mcd)	Voltage(V)
Red	620-625	390-420	2.0-2.2
Green	522-525	660-720	3.0-3.4
Blue	465-467	180-200	3.0-3.4



### Data transmission method:

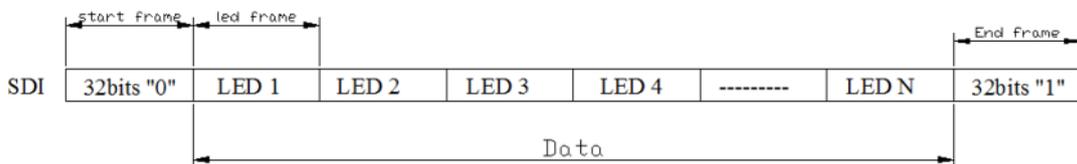
Note: The data of D1 is send by MCU, and D2, D3, D4 through pixel internal re shaping amplification to transmit.

Composition of 24bit data:

B7	B6	B5	B4	B3	B2	B1	B0	G7	G6	G5	G4	G3	G2	G1	G0	R7	R6	R5	R4	R3	R2	R1	R0
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Note: Follow the order of GRB to sent data and the high bit sent at first.

#### 1). Data Format:



Start frame 32bits	00000000	00000000	00000000	00000000
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Led frame 32bits	11111111	Blue	Green	Red
	8 bits	8 bits	8 bits	8 bits

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Data MSB—	Duty Cycle
00000000	0/256(min)
00000001	1/256
00000010	2/256
...	
11111101	253/256
11111110	254/256
11111111	255/256(max)

2).The number of pixels per second sent to CKI frequency ( FCKI ) minus the Start Frame bit divided by the number 40 the number of LED Frame bit 32, if CKI frequency ( FCKI ) to 512KHz, the pixel number ( 512000-40 ) /32=15998, if the 50 second update Views can be connected in series LED number 15998/50=319.To increase the number of cascaded IC CKI frequency to be increased.

(3).POLAR to empty , R, G, B for the negative output; POLAR access VSS, R, G, B is positive output.

(4).VEN: Self-detection

Data Field to the middle of 3bit were B, G, R in the MSB of the opposite phase, otherwise regarded as invalid data. VEN close to empty when the self-detection: when VEN VSS then activated self-detection.

(5).CSEL to empty when the CKO and CKI RP :CSEL connected with VSS when the CKO compared with CKI.

### Note:

1. Don't open the moisture proof bag before you ready use
2. The led should be kept at 30°C or less and 60%RH or less before opening package.
3. keeping led over 3 months or reuse led that is kept in open-package, Then please eliminate humid.  
Pls use the constant-temperature oven to toast the led at 60°C for 12Hours. or peel led from roll and toast it at 120°C for 2hours.
4. the temperature of iron be lower 300 °C and soldering within 3sec, Per solder-pad is observed.